

FIG.1

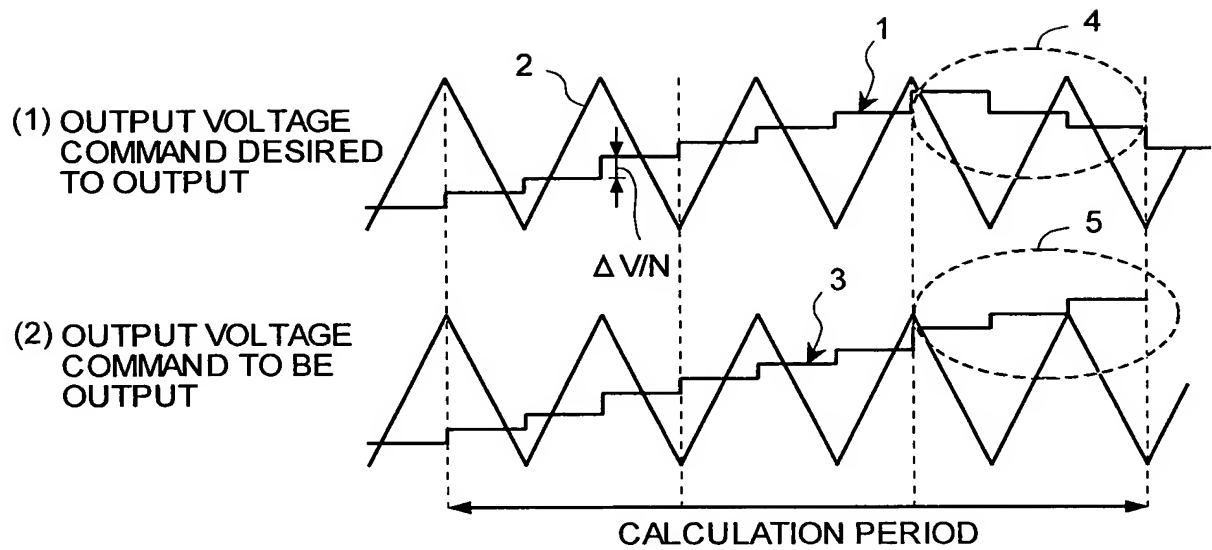


FIG.2

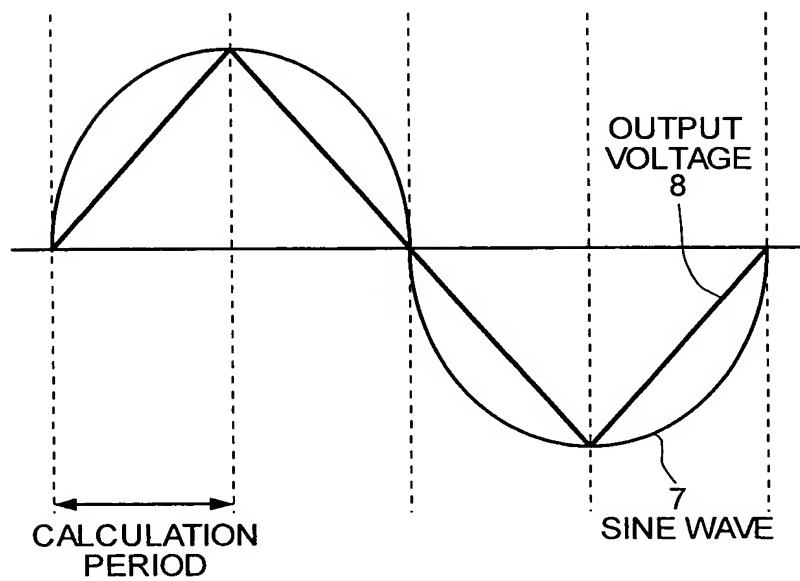


FIG.3

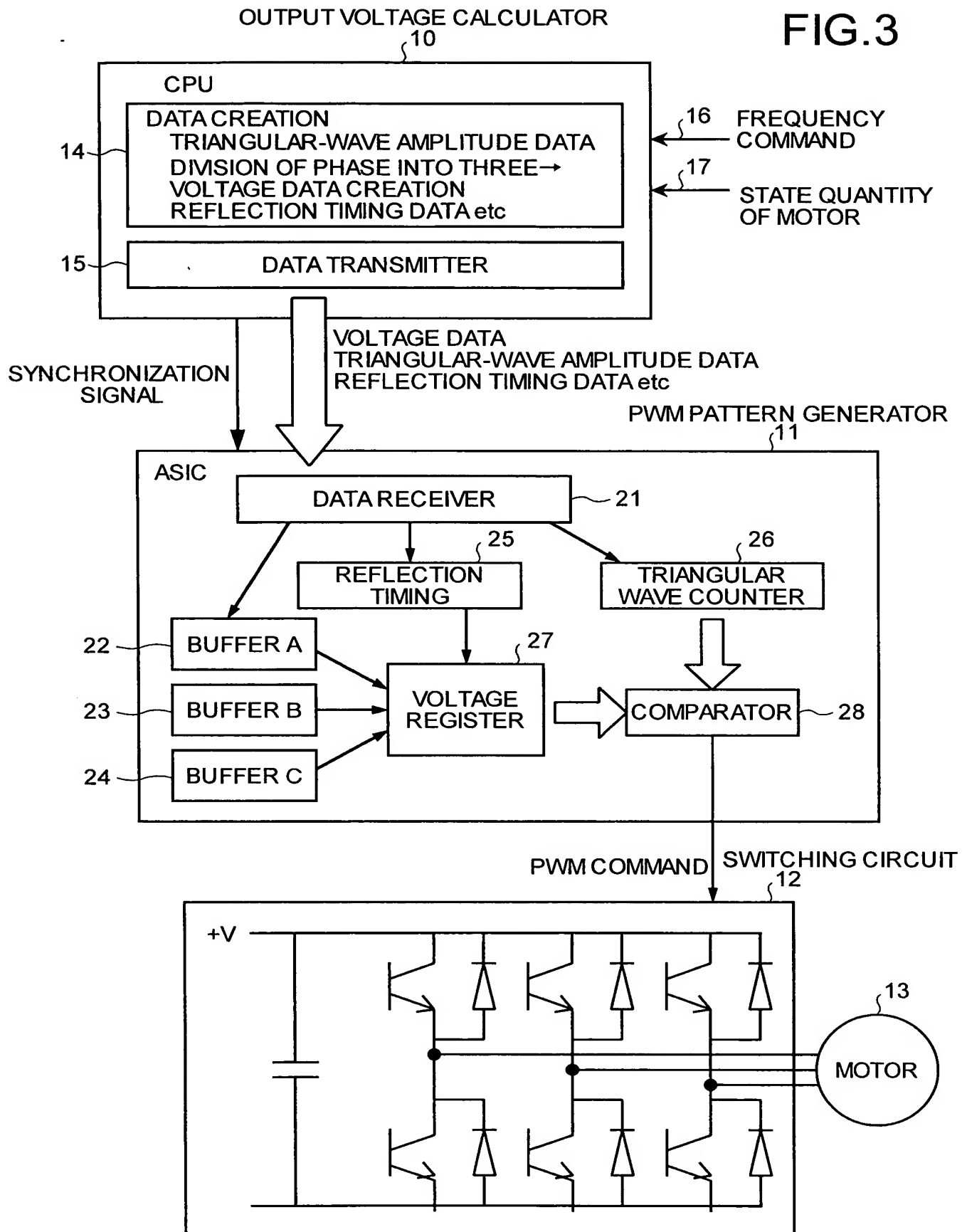


FIG.4

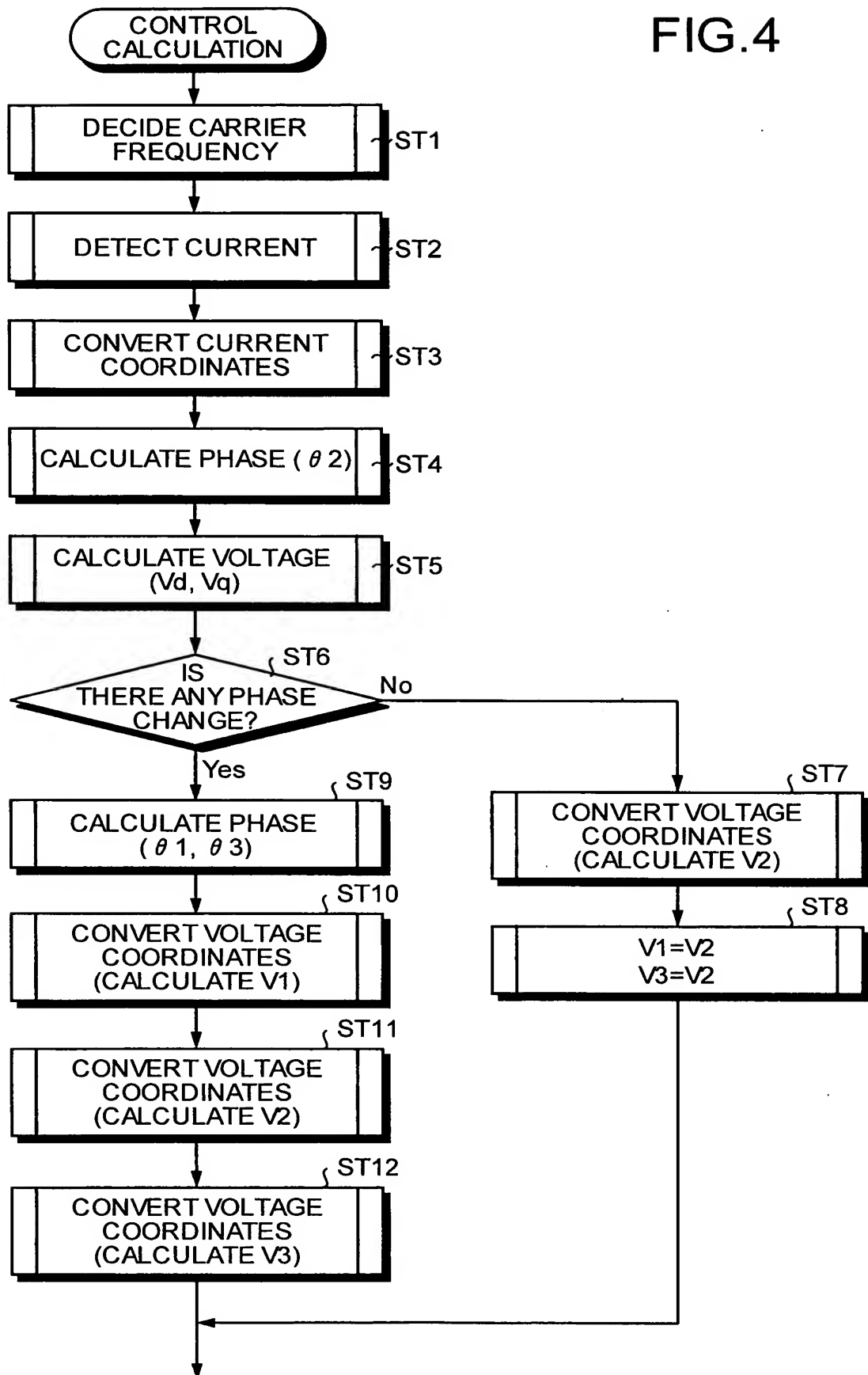


FIG.5

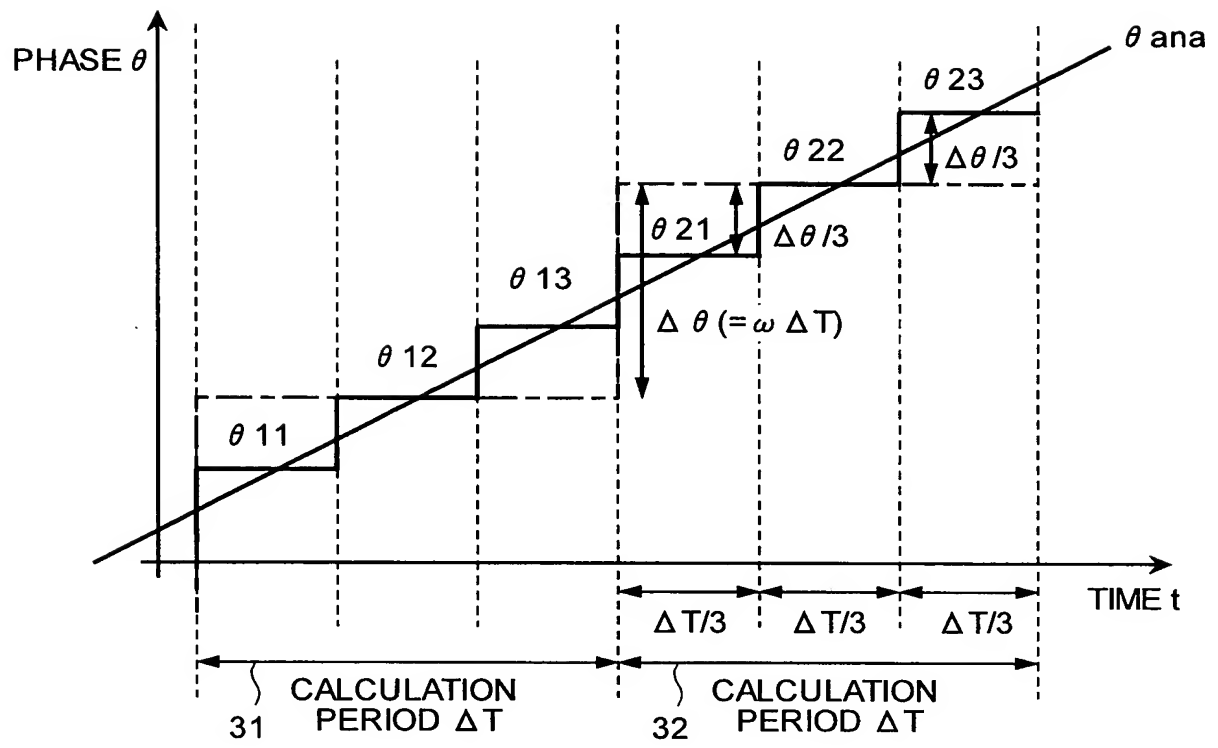


FIG. 6

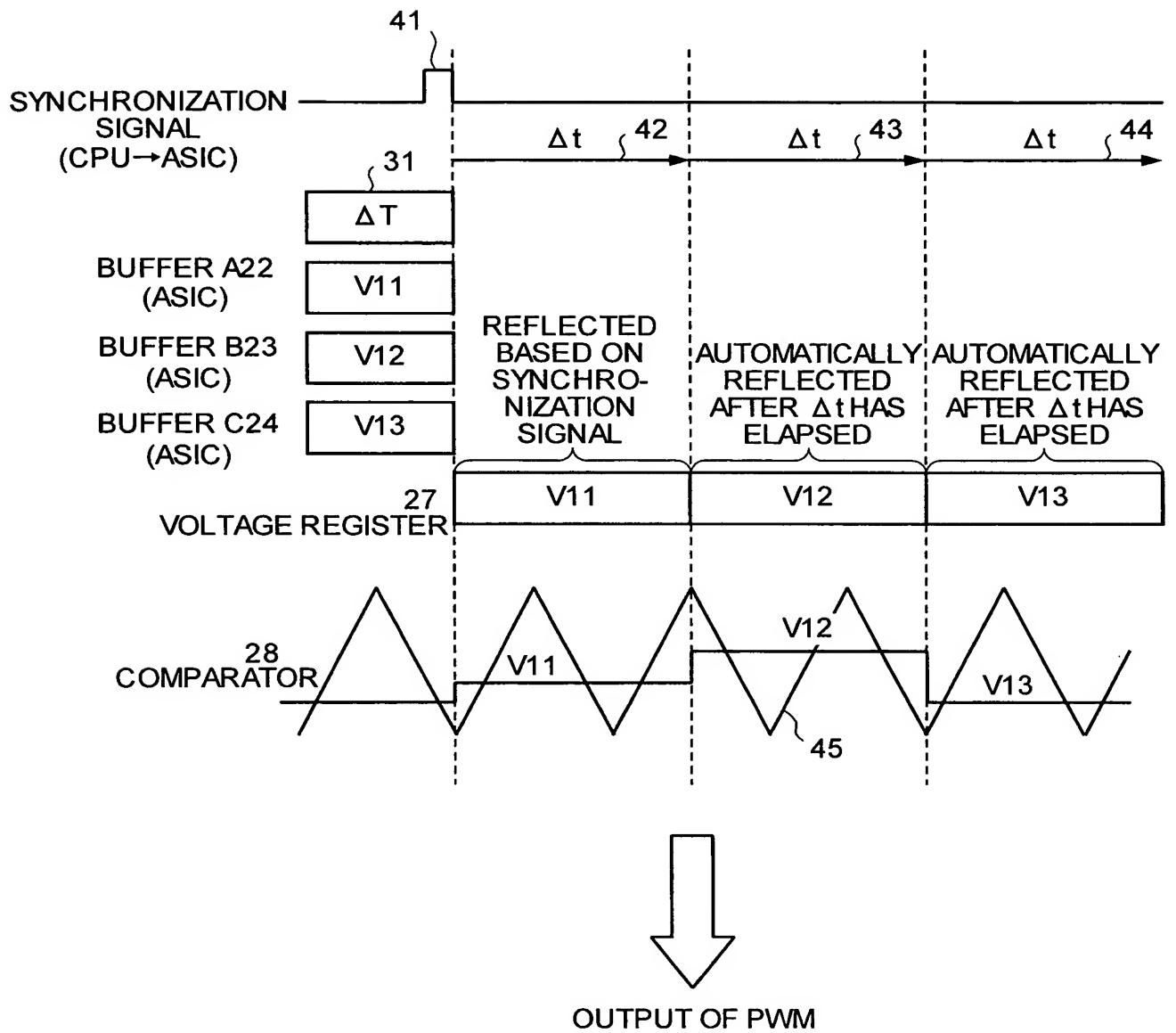
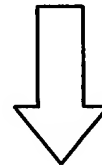
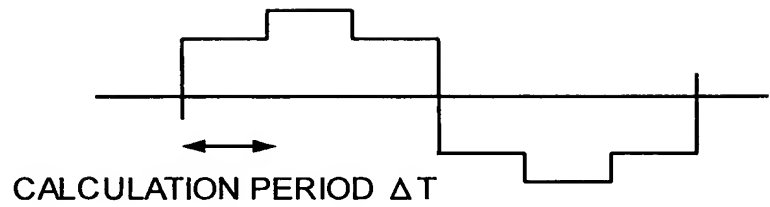


FIG.7

(1) WAVEFORM OF OUTPUT
VOLTAGE BASED ON
CONVENTIONAL
TECHNOLOGY



(2) WAVEFORM OF OUTPUT
VOLTAGE ACCORDING TO
THE EMBODIMENT

